

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application. Please amend claims 1 and 12 as follows:

LISTING OF CLAIMS:

1. (Currently Amended) An optical semiconductor package for packaging therein an optical semiconductor element, comprising:

a stem with a hole;

a dielectric sealed into the hole of the stem, and with a pair of pin insertion holes; and

a pair of high frequency signal pins that penetrate through and fit into the pair of pin insertion holes of the dielectric, and that constitute differential lines electrically connected to the optical semiconductor element;

wherein the pair of high frequency signal pins are coupled by an electric field coupling each other, one of the high frequency signal pins extracts a positive-phase current signal and the other high frequency signal pin applies a current signal opposite in phase to the positive-phase current signal.

2. (Original) The optical semiconductor package according to claim 1, wherein the dielectric is glass.

3. (Previously Presented) The optical semiconductor package according to claim 1, wherein the stem includes

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a first member arranged on an outside of the dielectric, wherein a coefficient of thermal expansion of the first member is equal to a coefficient of thermal expansion of the dielectric; and

a second member arranged on an outside of the first member, wherein a thermal conduction of the second member is higher than that of the first member.

4. (Original) The optical semiconductor package according to claim 1, wherein the dielectric is transparent or semitransparent.

5. (Original) The optical semiconductor package according to claim 1, wherein the hole in the stem has one of an oval shape, an elliptic shape, and a cocoon shape.

6. (Original) The optical semiconductor package according to claim 1, wherein a ground member in parallel to the pair of high frequency signal pins is provided on the stem.

7. (Original) The optical semiconductor package according to claim 6, wherein the ground member is a pair of ground pins, and the pair of ground pins are provided on outer sides of the pair of high frequency signal pins, respectively, so as to put the pair of high frequency signal pins between the pair of ground pins.

8. (Original) The optical semiconductor package according to claim 1, wherein the optical semiconductor element is a semiconductor laser diode including a pair of electrodes, and the optical semiconductor package further comprising:

a differential line substrate having a one end side connected to the pair of high frequency signal pins, and an other end side connected to the pair of electrodes of the optical semiconductor element;

a pair of inductance elements having one end sides connected to the pair of electrodes of the optical semiconductor element, respectively, and having other end sides connected to an external bias current source.

9. (Previously Presented) The optical semiconductor package according to claim 8, wherein stubs are formed on the pair of differential lines, respectively, and the pair of differential lines are formed on the differential line substrate.

10. (Original) The optical semiconductor package according to claim 9, wherein the stubs are formed to protrude in a direction in which the respective differential lines are closer to each other.

11. (Original) The optical semiconductor package according to claim 1, further comprising a cap that includes a light passing hole, and that airtight closes an internal space including the optical semiconductor element by fixing an end portion to the stem.

12. (Currently Amended) An optical semiconductor package that contains an optical semiconductor element and an integrated circuit which transmits and receives differential signals to and from the optical semiconductor element, the optical semiconductor package comprising:

a dielectric sealed into and fixed to a wall surface of the package, and having a pair of pin insertion holes; and

a pair of signal pins that penetrate through and fit into the pair of pin insertion holes, and constituting differential lines, wherein

differential signals are transmitted and received to and from the integrated circuit through the pair of signal pins;

wherein the pair of signal pins are coupled by an electric field coupling each other, one of the signal pins extracts a positive-phase current signal and the other signal pin applies a current signal opposite in phase to the positive-phase current signal.